step00 br(if notINTR then step00)

step01 br(if notprekid then **step11**)

step02 mxMAR2, ldMAR, decSP, mxMDR2, ldMDR

step03 wrMEM, br(if notFCBUS then step03)

step04 mxMAR2, ldMAR, decSP, mxMDR0, mxMDR1, ldMDR

step05 wrMEM, br(if notFCBUS then step05)

step06 mxMAR2, ldMAR, decSP, mxMDR0, mxMDR2, ldMDR

step07 wrMEM, br(if notFCBUS then step07)

step08 mxMAR2, ldMAR, decSP, mxMDR1, mxMDR2, ldMDR

step09 wrMEM, br(if notFCBUS then step09)

step0A ldBR

step0B mxMAR0, mxMAR2, ldMAR

step0C rdMEM, ldMDR, br(if notFCBUS then step0C)

step0D ldDW15..8, incMAR

step0E rdMEM, ldMDR, br(if notFCBUS then step0E)

step0F ldDW7..0

step10 mxPC1, ldPC

step11 stFETCH, clINTR